朝陽科技大學 097學年度第2學期教學大綱
Programmable IC Design 可程式積體電路設計

當期課號	2680	Course Number	2680
授課教師	曾國坤	Instructor	Tseng,kuo kun
中文課名	可程式積體電路設計	Course Name	Programmable IC Design
開課單位	資訊工程系(四日)三A	Department	
修習別	選修	Required/Elective	Elective
學分數	3	Credits	3
課程目標	本課程主要講述可程式積體電路的基本原理。並利用專題實作的過程，體驗整個可程式積體電路的設計流程。學生在完成本課程後，將可學習到以下之基本技能：1. 數位電路設計觀念、2. XILINX ISE 軟體操作、3. 硬體描述語言簡介、4. 專題實作。	Objectives	The goal of this course is to provide the students with a basic knowledge of FPGA design. By giving appropriate project assignments, the course helps the students experience the whole FPGA design flow. After finishing this course, the students may learn the following: 1. basic concepts for digital circuits design, 2. the application of XILINX ISE, 3. a brief introduction to VHDL, and 4. projects implementation.
教材	PFPGA-BASED SYSTEM DESIGN, WOLF (全華代理)	Teaching Materials	PFPGA-BASED SYSTEM DESIGN, WOLF
成績評量方式	Midterm: 35% Final: 35% Quiz: 10% Report: 10% Project: 10%	Grading	Midterm: 35% Final: 35% Quiz: 10% Report: 10% Project: 10%
教師網頁			
教學內容	1. Overview IC and VLSI Design 3. Overview FPGA Design 4. HDL Design 5. Combinational Circuit Design 6. Sequentail Circuit Design 7. Advacned Logic Design 8. Hardware Software Co-design 9. Project implementation	Syllabus	1. Overview IC and VLSI Design 3. Overview FPGA Design 4. HDL Design 5. Combinational Circuit Design 6. Sequentail Circuit Design 7. Advacned Logic Design 8. Hardware Software Co-design 9. Project implementation

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