# 朝陽科技大學 091學年度第2學期教學大綱
# Digital IC Design 數位積體電路設計

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| 當期課號 | 2638 | Course Number | 2638 |
| 授課教師 | 余昌峰 | Instructor | YU,CHANG FENG |
| 中文課名 | 數位積體電路設計 | Course Name | Digital IC Design |
| 開課單位 | 資訊工程系(四日)三A | Department | |
| 修習別 | 選修 | Required/Elective | Elective |
| 學分數 | 3 | Credits | 3 |
| 課程目標 | | Objectives | |
| 教材 | 板書、練習、測驗 | Teaching Materials | |
| 成績評量方式 | 期中考30% 期末考30% 平時考30% 出習率10% | Grading | Homework: 10% Midterm: 30% Fina 30% quiz 30% |
| 教師網頁 | | | |
| 教學內容 | 了解積體電路的設計方法和流程及與製程的相關關係。熟悉構成超大型積體電路的MOS電晶體理論、CMOS基本邏輯閘及基本邏輯電路 | Syllabus | The course explores the design aspects involved in the realization of CMOS integrated circuits/systems from device up to the register/subsystem level. It addresses major design methodologies with emphasis placed on structured full custom design. The course includes the study of MOS device characteristics, the critical interconnect and gate characteristics which determine the performance of VLSI circuits, and CMOS logic design. Students will use state-of-the art CAD tools to verify designs and develop efficient circuit layouts |

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