朝陽科技大學 091學年度第2學期教學大綱
Introduction to VLSI Design 超大型積體電路導論

當期課號	2624	Course Number	2624
授課教師	余昌峰	Instructor	YU,CHANG FENG
中文課名	超大型積體電路導論	Course Name	Introduction to VLSI Design
開課單位	資訊工程系(四日)二A	Department	
修習別	選修	Required/Elective	Elective
學分數	3	Credits	3
課程目標		Objectives	
教材		Teaching Materials	homework
成績評量方式	Homework: 40 % Midterm: 30 % Finalterm: 30 %	Grading	Homework: 40 % Midterm: 30 % Finalterm: 30 %
教師網頁			
教學內容	本課程以講授超大型積體電路基本理論爲主，內容包含了積體電路的原理、基本特性、電路設計以及輔助設計工具的應用等，使學生能充分的了解超大型積體電路的原理。	Syllabus	In this course we will study the principles and design of VLSI circuits.The contents of this course is composed of four parts, the principles of CMOS circuits, the structures of VLSI circuits, the architecture of VLSI Systems, and VLSI design methodology. In additional to the circuits and systems, we will also cover some CAD tools for the design of VLSI circuits.

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