朝陽科技大學 091學年度第1學期教學大綱
Analogy Integral Circuit Design 類比積體電路

當期課號	7388	Course Number	7388
授課教師	余昌峰	Instructor	YU,CHANG FENG
中文課名	類比積體電路	Course Name	Analogy Integral Circuit Design
開課單位	資訊工程系碩士班一A	Department	
修習別	選修	Required/Elective	Elective
學分數	3	Credits	3
課程目標	• Introduction to Analog Design • Basic MOS Device Physics • Single-Stage and Differential Amp. • Passive and Active Current Mirrors • Operational Amplifiers • Bandgap References • Switched-Capacitor Circuits • Oscillators • Phase-Locked Loops • ADC/DAC Converters • Layout and Package	Objectives	• Introduction to Analog Design • Basic MOS Device Physics • Single-Stage and Differential Amp. • Passive and Active Current Mirrors • Operational Amplifiers • Bandgap References • Switched-Capacitor Circuits • Oscillators • Phase-Locked Loops • ADC/DAC Converters • Layout and Package
教材	板書、投影機、hspice & cadence模擬	Teaching Materials	
成績評量方式	期中考30% 期末考30% 作業20% 出席率20%	Grading	
教師網頁	_		
教學內容	1.Models for IC activ devices 2. BJT,MOS and IC technology 3. Transistor amplifiers 4.Current source 5.Operational amplifiers 6.Frequency response	Syllabus	

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